

Appl. No. 10/648,336  
Amendment dated: December 21, 2005  
Reply to OA of: August 23, 2005

This listing of claims will replace all prior versions and listings of claims in the application.

**Listing of Claims:**

Claim 1(currently amended). A panel for an organic electroluminescent device, comprising:

a substrate having a first conducting area, a second conducting area, a third conducting area, and an active area; wherein said active area locates between said first conducting area and said second conducting area; said third conducting area locates at one side of said active area; said first conducting area, said second conducting area, said third conducting area and said active area are integrated together on the surface of said substrate; and said third conducting area locates adjacent to said first conducting area, said second conducting area, and said active area;

a plurality of first conducting lines located in said first conducting area on said substrate;

a plurality of second conducting lines located in said second conducting area on said substrate;

a plurality of third conducting lines located in said third conducting area on said substrate;

a plurality of first electrodes located in said active area, wherein said first electrode connects a third conducting line;

a plurality of second electrodes located in said active area, wherein said second electrode connects a first conducting line or a second conducting line; and at least one organic electroluminescent medium located in said active area, wherein said organic electroluminescent medium is sandwiched between said first electrode and said second electrode;

wherein said first conducting line connects a third conducting line, said

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second conducting line connects a third connecting line, said first ~~electrodes don't~~  
electrodes do not directly connect said second electrodes, and said first conducting  
lines, said second conducting lines, said third conducting lines and said first  
electrodes are on the surface of said substrate.

Claim 2(original). The panel as claimed in claim 1, further comprising a  
bonding unit located in said third conducting area for bonding at least one integrated  
circuit or a cable.

Claim 3(original). The panel as claimed in claim 1, wherein said integrated  
circuit or said cable is bonded to said bonding unit through chip on glass (COG)  
sealing.

Claim 4(original). The panel as claimed in claim 1, wherein the number of  
said first conducting lines is equal to that of said second conducting lines.

Claim 5(original). The panel as claimed in claim 1, wherein said conducting  
lines are selected from Al, Cr, Ag, and alloys thereof.

Claim 6(currently amended). The panel as claimed in claim 1, further  
comprising at least a pixel-defining layer located between said organic  
electroluminescent medium to define the pixel area of ~~said first electrode in~~ said  
active area.

Claim 7(original). The panel as claimed in claim 1, further comprising a  
plurality of auxiliary electrodes located on the surface of or embedded in said first  
electrodes, and said auxiliary electrodes are used to increase the current density of  
the first electrodes.

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Claim 8(original). The panel as claimed in claim 6, further comprising a plurality of isolating walls located on the surface of said pixel-defining layer.

Claim 9(original). The panel as claimed in claim 1, further comprising a barrier cover located above said active area for preventing said organic electroluminescent medium from the moisture, oxygen, oxide, or sulfide in the air, and said cover is bonded with said panel through sealing.

Claim 10(original). A panel for an organic electroluminescent device, comprising:  
a substrate having a first conducting area, a second conducting area, a third conducting area, a first film, and an active area; wherein said active area locates between said first conducting area and said second conducting area; said third conducting area locates at one side of said active area; said first conducting area, said second conducting area, said third conducting area and said active area are integrated together on the surface of said substrate; and said third conducting area located adjacent to said first conducting area, said second conducting area, and said active area;

a plurality of first conducting lines locates in said first conducting area on said substrate;

a plurality of second conducting lines locates in said second conducting area on said substrate;

a plurality of third conducting lines locates in said third conducting area on said substrate, said first conducting line connects a third conducting line; said second conducting line connects a third connecting line;

a plurality of first electrodes located in said active area, wherein said first electrode connects a third conducting line;

a plurality of second electrodes located in said active area, wherein said second electrode connects a first conducting line or a second conducting line;

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at least one organic electroluminescent medium located in said active area, wherein said organic electroluminescent medium is sandwiched between said first electrode and said second electrode; and

a first film embedded with a plurality of fourth conducting lines, wherein said fourth conducting lines are electrically connected with said third conducting lines, at least part of the pins of an integrated circuit, or a combination thereof.

Claim 11(original). The panel as claimed in claim 10, further comprising a printed circuit board, and said pins of said integrated circuit connect with said printed circuit board.

Claim 12(original). The panel as claimed in claim 11, wherein said pins of said integrated circuit and said printed circuit board are connected through anisotropic conductive films.

Claim 13(original). The panel as claimed in claim 10, further comprising a printed circuit board and a second films, wherein part of said pins of said integrated circuit connects with said second film, said printed circuit board electrically connects said second films.

Claim 14(original). The panel as claimed in claim 10, further comprising a printed circuit board and a second films, wherein part of said pins extends from said integrated circuit and is embedded in said second film, and said extended pins embedded in said second film connect to said printed circuit board.

Claim 15(original). The panel as claimed in claim 11, wherein said integrated circuit and said printed circuit board are connected through anisotropic conductive films or wire bonding

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Claim 16(original). The panel as claimed in claim 10, wherein the number of said first conducting lines is equal to that of said second conducting lines.

Claim 17(original). The panel as claimed in claim 10, wherein said conducting lines are selected from Al, Cr, Ag, and alloys thereof.

Claim 18(original). The panel as claimed in claim 10, further comprising at least one pixel-defining layer located between said organic electroluminescent medium to define the pixel area of said first electrode in said active area.

Claim 19(original). The panel as claimed in claim 10, further comprising a plurality of auxiliary electrodes located on the surface of or embedded in said first electrodes, and said auxiliary electrodes are used to increase the current density of the first electrodes.

Claim 20(original). The panel as claimed in claim 18, further comprising a plurality of isolating walls located on the surface of said pixel-defining layer.

Claim 21(original). The panel as claimed in claim 10, further comprising a barrier cover located above said active area for preventing said organic electroluminescent medium from the moisture, oxygen, oxide, or sulfide in the air, and said cover is bonded with said panel through sealing.

Claim 22(original). The panel as claimed in claim 10, further comprising a plurality of fifth conductive lines embedded in said first film, wherein said fourth conducting lines and said fifth conducting lines do not connect to each other directly, part of pins of said integrated circuit electrically connects to said fourth conducting lines, and the other part of pins of said integrated circuit electrically connects to said fifth conducting lines.

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Claim 23(original). The panel as claimed in claim 10, wherein said first film is a hard film for tape carrier package (TCP).

Claim 24(original). The panel as claimed in claim 10, further comprising plural electrically passive devices bonded on said fourth conducting lines on said first film.

Claim 25(original). The panel as claimed in claim 24, wherein said electrically passive devices are capacitors or resistors.